		 	 		<del></del> -	 
USN			 _	l .		

## Fourth Semester B.E. Degree Examination, Dec.2013/Jan.2014 Computer Organization

Time: 3 hrs.

Max. Marks: 16

Note: Answer FIVE full questions, selecting at least TWO questions from each part.

## PART - A

		the basic functional unit of a computer.	(10 Marks)
1	a.	Explain with necessary block diagram the basic functional unit of a computer.	(10 Marks)
	b.	Explaining detail the generation of computers.	•

Big-Endian and Little-Endian assignments, explain with necessal (05 Marks) 2 a.

List the name, assembler syntax and addressing functions for the different addressing modes. b. (05 Marks)

Explain logical and arithmetic shift instructions with an example. Ç.

Draw the arrangement of a single bus structure and brief about memory mapped I/O. (05 Marks) 3 a.

Explain: i) Interrupt enabling; iii) Interrupt leabling; iii) Edge triggering, with respect b. interrupts.

or using a daisy chain and explain in brief. (05 Marks) Draw the arrangement for bus arbitra

Draw the hardware components needed for connecting a keyboard to a process and explain 4

in brief. Explain the use of PCI bas in a computer system with necessary figure. (05 Marks) (05 Marks) b.

List the SCSI bus signals with their functionalities.

## PART – B

		O" id a Fours	(10 Marks)
_	_	Draw for 1K x themory chip with neat lighte.	(05 Marks)
5	a.	Okam with diggram the memory hierarchy with respect to speed, size and cook	(05 Marks)
	b	Show with that direct manning cache memory.	(02 MINING)
J	b.	Draw for 1K × Themory chip with neat figure.  Show with diagram the memory hierarchy with respect to speed, size and cost.	

With figure explain about direct mapping cache memory. ¢.

(10 Marks) Explain with figure the design of a 4-bit carry look ahead adder. (05 Marks) With figure explain circuit arrangements for binary division. a. (05 Marks)

b. IEEE standard for floating point numbers, explain.

Draw and explain the single-bus organization of the data path inside a processor.

Write the control sequence for an un-conditional branch instruction.

Draw the block diagram of the control unit organization and describe in brief.

Explain in brief about multiprocessor systems. 8 a.

Explain about memory organization in multiprocessors. b.

What are the classifications of parallel processing?

(05 Marks)

(10 Marks)

(05 Mapks)

(05 Marks)

(10 Marks)

(05 Marks)